

WHAT IS CLAIMED IS:

1. A wireless communications device performing wireless communication, comprising:
 - a transceiver for transmitting and receiving data externally; and
- 5 a controller for processing the data received from the transceiver, the wireless communications device operating as a slave that is connected to a master, said controller receiving polling data addressed to the wireless communications device from said master through the transceiver, and temporarily stopping the operation of the transceiver for a sleep period which is determined according to the number of other slaves connected to the master.
2. The device as claimed in claim 1, wherein the controller calculates the sleep period by multiplying a predetermined time slot by double the number of the other slaves.
3. The device as claimed in claim 2, wherein the predetermined time slot corresponds to 625 μ second.
4. The device as claimed in claim 1, wherein the master sequentially transmits said polling data according to a predetermined slave order, and the controller, upon receipt of the polling data addressed to the device, stops the operation of the transceiver for the sleep period after a completion of the data transmission.
5. A controlling method for a wireless communications device, comprising the steps of:
 - i) detecting whether polling data addressed to the device is received from a master;

ii) transmitting data to the master when the polling data addressed to the device is received, and stopping a data reception from the master for a predetermined sleep period;

5 iii) identifying whether the sleep period has elapsed; and

 iv) repeating the steps i) and ii) at least once, when determining the sleep period has elapsed.

6. The method as claimed in claim 5, wherein the sleep period is calculated by multiplying the predetermined time slot by two times the number of other slaves connected to the master, using connection state information received from the master.

7. The method as claimed in claim 6, wherein the time slot corresponds to 625μ second.